



Docket No.: 1086.1141

2186  
#101A  
3-3-04  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Mitsuru SATO, et al.

Serial No. 09/786,049

Group Art Unit: 2186

Confirmation No. 6861

Filed: June 18, 2001

Examiner: Hetul B. Patel

For: CACHE DEVICE AND CONTROL METHOD FOR CONTROLLING CACHE MEMORIES  
IN A MULTIPROCESSOR SYSTEM (as amended)

AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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MAR 02 2004

Sir:

TC 2100

This is in response to the Office Action that was mailed on November 14, 2003, which has a period for response that is set to expire on February 14, 2004. Because February 14, 2004 is a Saturday, this Amendment is timely filed by Monday, February 16, 2004.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

Amendments to the Specification begin on page 2 of this Amendment.

Amendments to the claims begin on page 3 of this Amendment.

Remarks begin on page 8 of this Amendment.

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GROUP 3600